

What is claimed:

1. A method for operating a display having a plurality of pixel elements comprises:

- a) applying a transition voltage to the plurality of pixel elements;
- b) applying a first paint voltage to one pixel element of the plurality pixel elements; thereafter
- c) waiting a predetermined time period; thereafter
- d) illuminating the one pixel element; thereafter
- e) applying the transition voltage to the plurality of pixel elements;
- f) applying a second paint voltage to the one pixel element elements; thereafter
- g) waiting the predetermined time period; and thereafter
- h) illuminating the one pixel element;

wherein the transition voltage is between the first paint voltage and the second paint voltage applied to the one pixel element.

2. The method of claim 1 wherein d) comprises illuminating the one pixel element with an illumination source of a first color, and h) comprises illuminating the one pixel element with an illumination source of a second color.

3. The method of claim 1 further comprising after h):

- i) applying the transition voltage to the plurality of pixel elements;
- j) applying a third paint voltage to the one pixel element elements; thereafter
- k) waiting the predetermined time period; and thereafter
- l) illuminating the one pixel element,

wherein d) comprises illuminating the one pixel element with an illumination source of a first color, h) comprises illuminating the one pixel element with an illumination source of a second color, and l) comprises illuminating the one pixel element with an illumination source of a third color.

1 *Sub*
2 *C1* 4. The method of claim 3 wherein the first color, the second color,
3 and the third color are selected, without replacement, from the group: red color, green
color, blue color.

1 5. The method of claim 1 wherein d) comprises illuminating the
2 one pixel element with an illumination source and both comprises illuminating the
3 one pixel element with the illumination source.

1 6. The method of claim 1 wherein applying the transition voltage
2 to the plurality of pixel elements comprises applying the transition voltage to all of the
3 plurality of pixel elements at one time.

1 7. The method of claim 1 wherein applying the transition voltage
2 to the plurality of pixel elements comprises:
3 applying the transition voltage to a first row of pixel elements from the
4 plurality of pixel elements; and thereafter

5 applying the transition voltage to a second row of pixel elements from
6 the plurality of pixel elements.

1 8. The method of claim 1 wherein applying the transition voltage
2 to the plurality of pixel elements comprises:
3 applying the transition voltage to a first column of pixel elements from
4 the plurality of pixel elements; and thereafter

5 applying the transition voltage to a second column of pixel elements
6 from the plurality of pixel elements.

1 9. A display having a plurality of pixel elements comprises:
2 a transaction circuit coupled to each pixel element in the plurality of
3 pixel elements, the transaction circuit configured to apply a transition voltage to the
4 plurality of pixel elements;

5 a paint circuit coupled to the transaction circuit, the paint circuit
6 configured to apply a first paint voltage and a second paint voltage to one pixel
7 element from the plurality of pixel elements after the transition voltage is applied to
8 the plurality of pixel elements;

a timer circuit coupled to the paint circuit, the timer circuit configured to determine when a predetermined time period has elapsed;

an illumination circuit coupled to the timer circuit, the illumination circuit configured to illuminate the one pixel element after the predetermined time period has elapsed;

wherein the transition voltage is applied to the plurality of pixel elements before the first paint voltage is applied to the plurality of pixel elements, and wherein the transition voltage is applied to the plurality of pixel elements after the one pixel element is illuminated and before the second paint voltage is applied to the plurality of pixel elements, and

10. The display of claim 9

wherein the illumination circuit is configured to illuminate the one pixel element with a first color after the first paint voltage is applied to the one pixel element, and

wherein the illumination circuit is configured to illuminate the one pixel element with a second color after the second paint voltage is applied to the pixel element.

11. The display of claim 9

wherein the paint circuit is also configured to apply a third paint voltage to one pixel element from the plurality of pixel elements after the transition voltage is applied to the plurality of pixel elements;

wherein the transition voltage is applied to the plurality of pixel elements after the one pixel element is illuminated and before the third paint voltage is applied to the plurality of pixel elements;

wherein the illumination circuit is configured to illuminate the one pixel element with a first color after the first paint voltage is applied to the plurality of pixel elements;

wherein the illumination circuit is configured to illuminate the one pixel element with a second color after the second paint voltage is applied to the plurality of pixel elements; and

14 wherein the illumination circuit is configured to illuminate the one
 15 pixel element with a third color after the third pixel voltage is applied to the plurality
 16 of pixel elements.

1 12. The display of claim 11 wherein the first color, the second
 2 color, and the third color are selected, without replacement, from the group: red color,
 3 green color, blue color.

1 13. The display of claim 9 wherein the illumination circuit
 2 comprises a monochromatic illumination source.

1 14. The display of claim 9 wherein applying the transition voltage
 2 to the plurality of pixel elements comprises applying the transition voltage to all of the
 3 plurality of pixel elements at one time.

1 15. The display of claim 9 wherein the transaction circuit is
 2 configured to apply the transition voltage to a first row of pixel elements from the
 3 plurality of pixel elements before a second row of pixel elements from the plurality of
 4 pixel elements.

1 16. The display of claim 9 wherein the transaction circuit is
 2 configured to apply the transition voltage to a first column of pixel elements from the
 3 plurality of pixel elements before a second column of pixel elements from the
 4 plurality of pixel elements.

1 17. A circuit for driving a liquid crystal display having a plurality
 2 of pixels comprises:
 3 a initializing circuit coupled to the plurality of pixels configured to
 4 apply an initial voltage to the plurality of pixels;
 5 a driving circuit coupled to the initializing circuit configured to apply a
 6 first drive voltage and a second drive voltage to a pixel from the plurality of pixels
 7 after the initial voltage has been applied to the plurality of pixels; and
 8 an illumination circuit coupled to the driving circuit configured to
 9 illuminate the pixel a predetermined time period after the pixel has been driven with
 10 first drive voltage and after the pixel has been driven with the second drive voltage;

wherein the initial voltage is applied to the plurality of pixels before the pixel is driven with the first drive voltage, and

wherein the initial voltage is applied to the plurality of pixels before the second drive voltage is applied to the plurality of pixels.

18. The circuit of claim 17

wherein the illumination circuit is configured to illuminate the pixel with a first color after the first drive voltage has been applied to the pixel, and

wherein the illumination circuit is configured to illuminate the pixel with a second color after the second drive voltage has been applied to the pixel.

~~19. The circuit of claim 17 wherein the first color and the second color are selected, without replacement, from the group: red color, green color, blue color.~~

20. The circuit of claim 17 wherein the initializing circuit is configured to apply an initial voltage to all of the pixels in the plurality of pixels at one time.

21. The circuit of claim 17 wherein the initial voltage is between the first drive voltage and the second drive voltage.